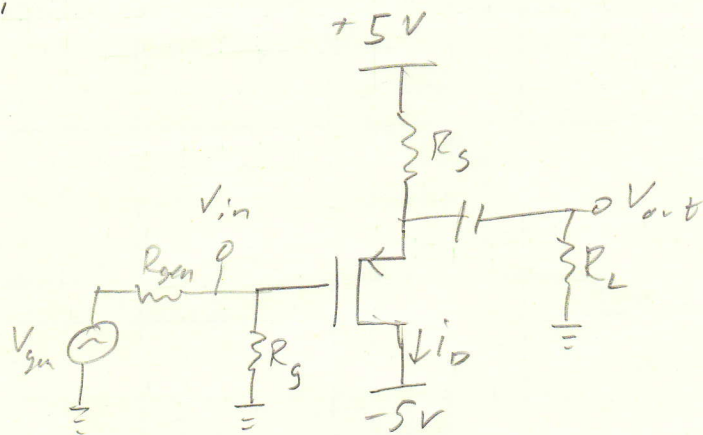


1.



$$t_{ox} = 10^{-7} \text{ cm}$$

$$|V_t| = 0.35 \text{ V}$$

$$\mu_p = 200 \text{ cm}^2/\text{V}\cdot\text{s}$$

$$R_L = 50 \text{ k}\Omega$$

$$R_{gen} = 100 \text{ k}\Omega$$

$$R_S = 1 \text{ M}\Omega$$

$$L_S = 130 \text{ nm}$$

$$\lambda = 0$$

a) given $i_D = 1/2 \text{ mA}$; $|V_{GS}| = 0.6 \text{ V}$

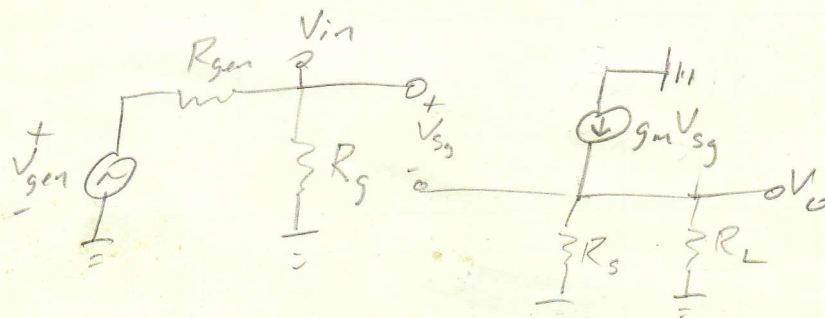
$$i_D = 1/2 \mu_p C_{ox} W/L (V_{GS} - V_{th})^2 = 1/2 \text{ mA}; \quad C_{ox} = \frac{\epsilon_{ox}}{t_{ox}} = \frac{3.9 \epsilon_0}{t_{ox}}$$

$$\Rightarrow W = 3 \mu\text{m}$$

b) For DC bias $V_G = 0$; $\Rightarrow V_S = +0.6 \text{ V}$

$$R_S = \frac{(5 - 0.6) \text{ V}}{1/2 \text{ mA}} = 8.8 \text{ k}\Omega$$

c)



$$g_m V_{sg} = V_o/R_S + V_o/R_L; \quad V_{in} = V_{sg} + V_o$$

$$\Rightarrow V_o/V_{in} = g_m / (g_m + 1/R_S + 1/R_L); \quad \text{Clearly } V_{in}/V_{gen} = \frac{R_g}{R_S + R_{gen}}$$

$$g_m = \frac{2I_D}{|V_{GS} - V_{th}|}$$

1.c) (cont.) $\boxed{V_o/V_{in} = 0.968} \quad \boxed{V_{in}/V_{gs} = 0.91}$
 $\rightarrow \boxed{V_o/V_{gs} = 0.88}$

d) Clearly $\boxed{R_{in} = R_g = 1M\Omega}$
 $R_{out} = R_s \parallel R_L \parallel 1/g_m = 242\Omega$

e) When transistor is off,

$$V_{out}^{max} = V_{ss} \left(\frac{R_L}{R_L + R_s} \right) = 5V \left(\frac{50k\Omega}{50k\Omega + 8.8k\Omega} \right) = \underline{4.25}$$

When transistor is in saturation,

$$|V_{ds}| = |V_{ds}| - |V_{tr}|$$

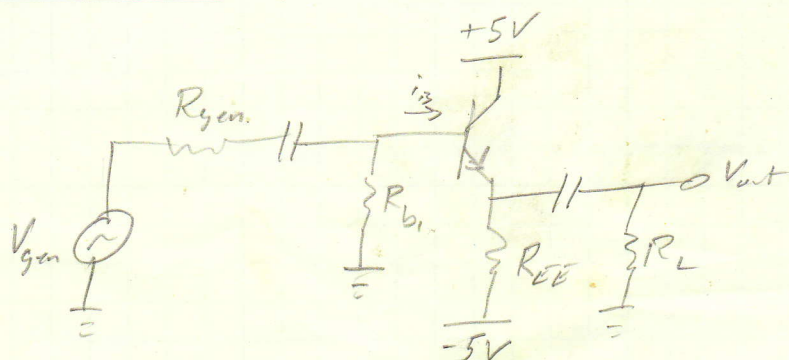
$$|-5V - V_o| = |V_{in} - V_o| - |0.35|$$

$$\begin{aligned} V_o + 5V &= |V_{in}/V_o \cdot V_o - V_o| - |0.35| \\ &= |V_o| (1/A_v - 1) - 0.35 \end{aligned}$$

$$\boxed{V_o^{min} = -4.76V}$$

Sent email for clarifications on voltage swings.

2.



$$\begin{aligned}\beta &= 100 \\ V_A &= 100V \\ i_E &= 1mA \\ R_{gm} &= 10k\Omega \\ R_L &= 500\Omega\end{aligned}$$

a) if $V_b = -100mV$: DC Bias

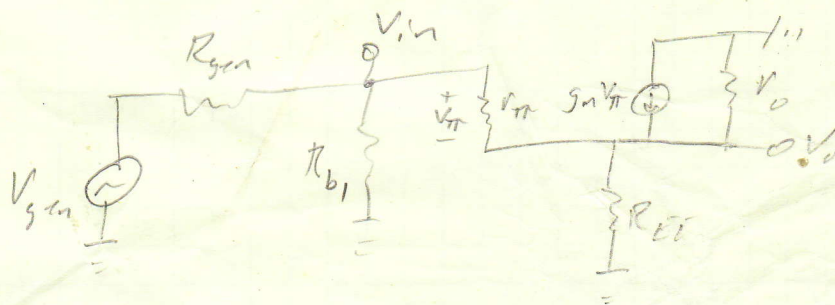
$$\rightarrow i_B = i_E / \beta = 10\mu A$$

$$V_B = -i_B \cdot R_{b1} \Rightarrow R_{b1} = 10k\Omega$$

$$V_{BE} = 0.7V \Rightarrow V_E = -800mV$$

$$R_{EE} = \frac{-800mV + 5V}{i_E} = 4.2k\Omega = R_{EE}$$

b)



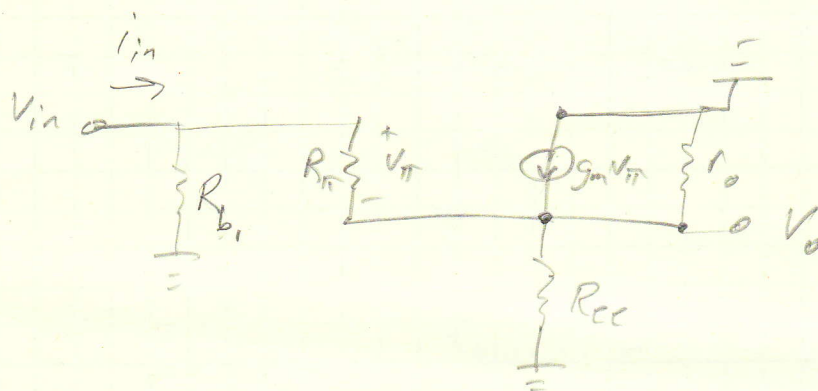
$$\rightarrow V_i = V_{\pi} + V_o ; g_m V_{\pi} = V_o / R_{EE} + V_o / r_o - V_{\pi} / r_{\pi}$$

$$\frac{V_o / V_{in}}{1/r_o + 1/R_{EE} + g_m + 1/r_{\pi}} = \frac{g_m + 1/r_{\pi}}{1/r_o + 1/R_{EE} + g_m + 1/r_{\pi}} ; r_{\pi} = \beta / g_m ; g_m = I_C / V_T$$

$$r_o \approx V_A / I_C$$

$$\rightarrow \frac{V_o}{V_{in}} = 0.998$$

2. (cont.)

Seek input impedance

$$i_{in} = V_{in}/R_{b1} + V_{\pi}/r_{\pi} ; V_{in} = V_{\pi} + V_o$$

$$g_m V_{\pi} + V_{\pi}/r_{\pi} = V_o/R_{ee} + V_o/r_o \rightarrow V_{\pi} = \frac{V_o (1/R_{ee} + 1/r_o)}{(g_m + 1/r_{\pi})}$$

$$V_{in} = V_{\pi} + V_{\pi} \frac{(g_m + 1/r_{\pi})}{(1/R_{ee} + 1/r_o)} \Rightarrow V_{\pi} = V_{in} \left[1 + \frac{g_m + 1/r_{\pi}}{1/R_{ee} + 1/r_o} \right]^{-1}$$

$$V_{\pi} = V_{in} \left[\frac{1/R_{ee} + 1/r_o}{1/R_{ee} + 1/r_o + g_m + 1/r_{\pi}} \right]$$

$$i_{in} = V_{in} \left[1/R_{b1} + \frac{1}{r_{\pi}} \left(\frac{1/R_{ee} + 1/r_o}{1/R_{ee} + 1/r_o + g_m + 1/r_{\pi}} \right) \right]$$

$$i_{in} = V_{in} \left[1/R_{b1} + \frac{1}{r_{\pi}} \left(\frac{r_{\pi} r_o + R_{ee} r_{\pi}}{r_{\pi} r_o + R_{ee} r_{\pi} + g_m r_{\pi} r_o R_{ee} + r_o R_{ee}} \right) \right]$$

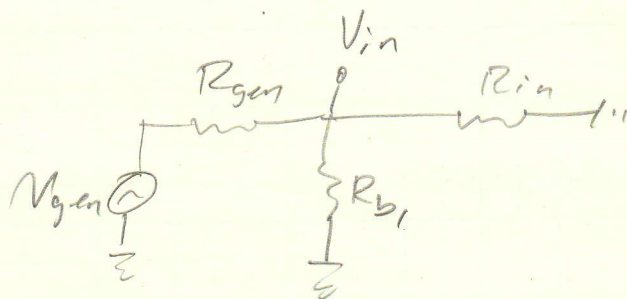
$$V_{in}/i_{in} = \left[1/R_{b1} + \frac{r_o + R_{ee}}{r_{\pi} r_o + R_{ee} r_{\pi} + g_m r_{\pi} r_o R_{ee} + r_o R_{ee}} \right]^{-1}$$

$$r_{\pi} = \beta/g_m ; g_m = i_c/V_T ; r_o = V_A/i_c \Rightarrow \begin{pmatrix} r_{\pi} \\ g_m \\ r_o \end{pmatrix} = \begin{pmatrix} 5180 \\ 0.0193 \\ 200 \text{ k}\Omega \end{pmatrix}$$

$$\Rightarrow \boxed{R_{in} = V_{in}/i_{in} = 9.77 \text{ k}\Omega}$$

2. (cont.)

Now:

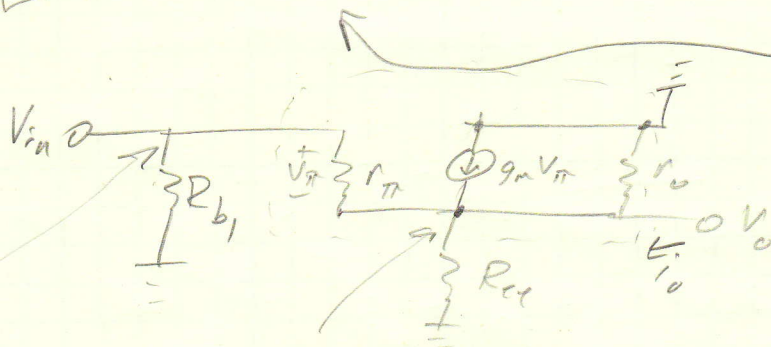


$$\Rightarrow V_{in} = V_{gen} \left(\frac{R_{bi} \parallel R_{in}}{R_{gen} + R_{bi} \parallel R_{in}} \right) = V_{gen} (0.331)$$

$$\Rightarrow \boxed{V_{in}/V_{gen} = 0.331}$$

$$\Rightarrow \boxed{V_{out}/V_{gen} = V_{out}/V_{in} \times V_{in}/V_{gen} = 0.327}$$

c) $\boxed{R_{in} = 9.77 k\Omega}$ (from before)



High input impedance

Low output impedance

Common collector makes good buffer

$$i_o + g_m V_{\pi} = V_o/r_o + V_o/R_{ce} - V_{\pi}/r_{\pi}$$

$$V_{in}/R_{bi} + V_{\pi}/r_{\pi} = 0 \quad ; \quad V_{in} = V_{\pi} + V_o$$

$$\Rightarrow \boxed{V_o/i_o = \left[1/R_{ce} + \frac{\beta}{\beta + g_m R_{bi}} (g_m/\beta + g_m) \right]^{-1} = 145 \Omega = R_{out}}$$

2. (cont.)

when transistor is off...

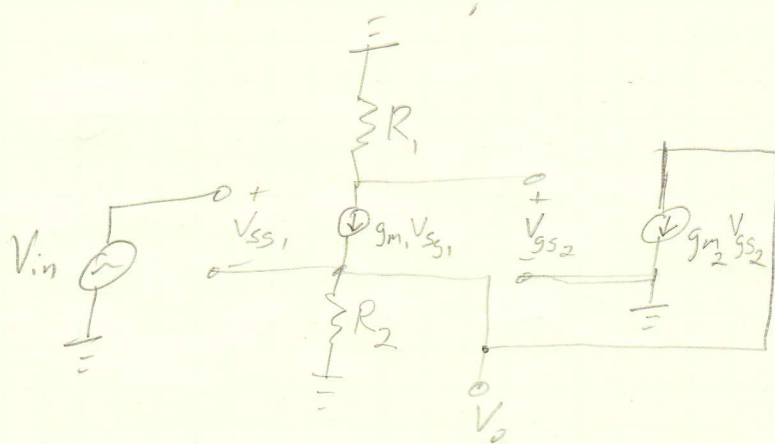
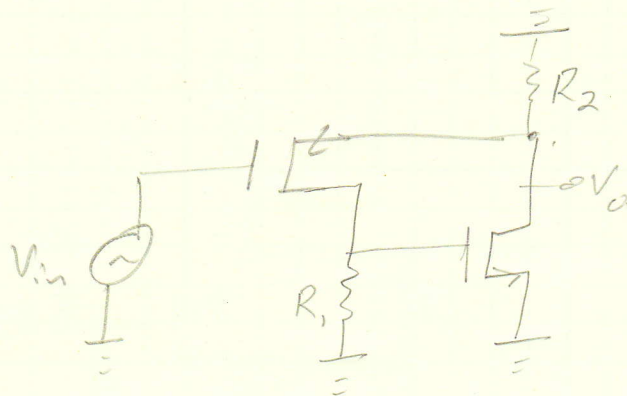
$$V_{out}^{min} = -5V \left(\frac{R_L}{R_L + R_{E1}} \right) = -0.532V$$

At saturation...

$$5 - V_{out}^{max} = V_{CE} = 0.5V \Rightarrow \underline{V_{out}^{max} = 4.5V}$$

Send email for clarification
on voltage swings.

3.



$$i) V_{in} = V_{gs1} + V_o \quad ; \quad ii) g_{m1}V_{gs1} = V_o/R_2 + g_{m2}V_{gs2}$$

$$iii) V_{gs2}/R_1 + g_{m1}V_{gs1} = 0 \Rightarrow V_{gs2} = -R_1 g_{m1} V_{gs1}$$

$$\Rightarrow g_{m1}V_{gs1} = V_o/R_2 - g_{m1}g_{m2}R_1 V_{gs1}$$

$$V_{gs1} = \left(\frac{V_o}{R_2} \right) \left(\frac{1}{g_{m1} + g_{m1}g_{m2}R_1} \right)$$

$$\Rightarrow V_{in} = V_o \left(1 + \frac{1}{R_2(g_{m1} + g_{m1}g_{m2}R_1)} \right) = V_o \left(\frac{R_2g_{m1} + R_1R_2g_{m1}g_{m2} + 1}{R_2g_{m1} + R_1R_2g_{m1}g_{m2}} \right)$$

$$V_o/V_{in} = \frac{R_1R_2g_{m1}g_{m2} + R_2g_{m1}}{R_1R_2g_{m1}g_{m2} + R_2g_{m1} + 1} = 0.9995$$