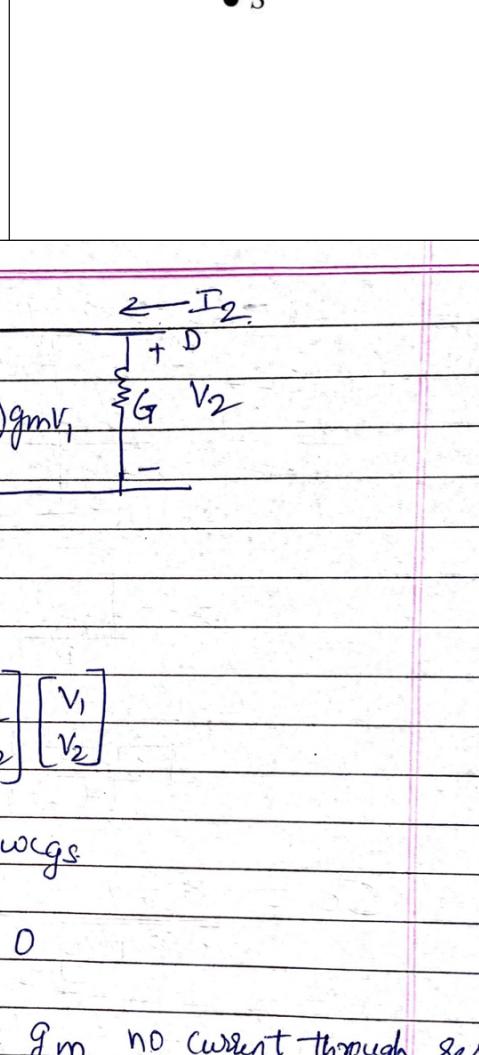


Problem 1: Shown is a highly simplified small-signal model of a MOSFET.  $R_i=0$  Ohms,  $gm=200$  mS,  $R_{ds}=75$  Ohms,  $C_{gs}=100$  fF,  $C_{gd}=0$  fF. The source is grounded.



- a) Compute by hand the four Y-parameters as a function of frequency.  
b) create a 2-port circuit of this device in ADS. Simulate using the provided gain\_testbench, and make plots of the real and imaginary parts of the 4 Y-parameters. Use linear scales for both axes.

1a)

$$I_1 = j\omega C_{gs} V_1$$

$$\begin{bmatrix} I_1 \\ I_2 \end{bmatrix} = \begin{bmatrix} Y_{11} & Y_{12} \\ Y_{21} & Y_{22} \end{bmatrix} \begin{bmatrix} V_1 \\ V_2 \end{bmatrix}$$

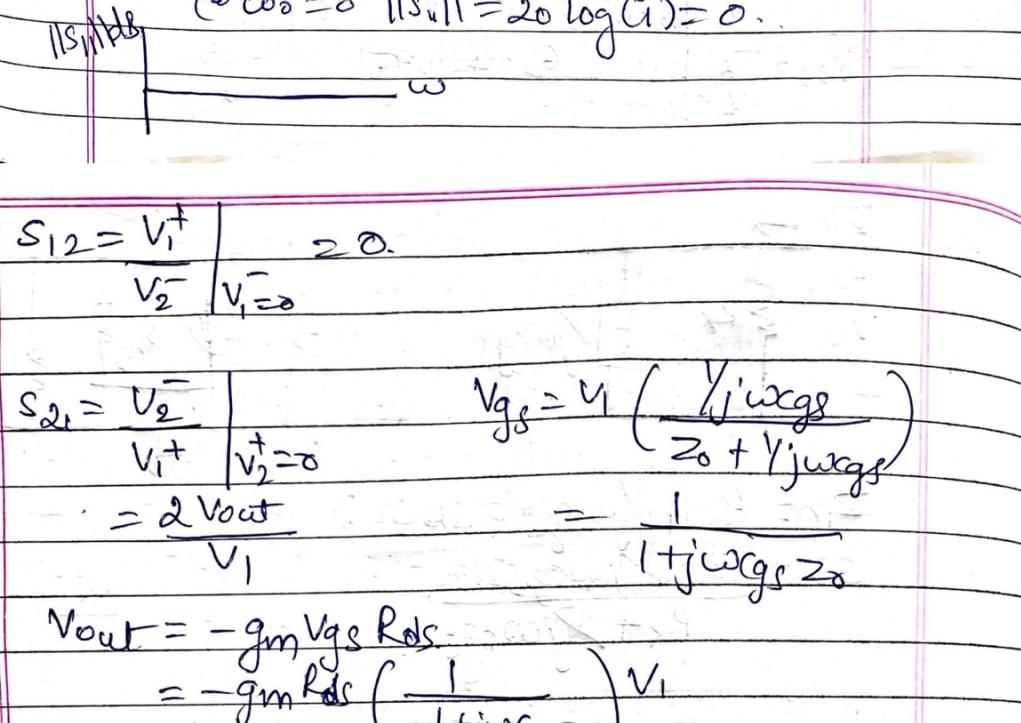
$$Y_{11} = \frac{I_1}{V_1} \Big|_{V_2=0} = j\omega C_{gs}$$

$$Y_{12} = \frac{I_1}{V_2} \Big|_{V_1=0} = 0$$

$$Y_{21} = \frac{I_2}{V_1} \Big|_{V_2=0} = g_m \text{ no current through } S \text{ port.}$$

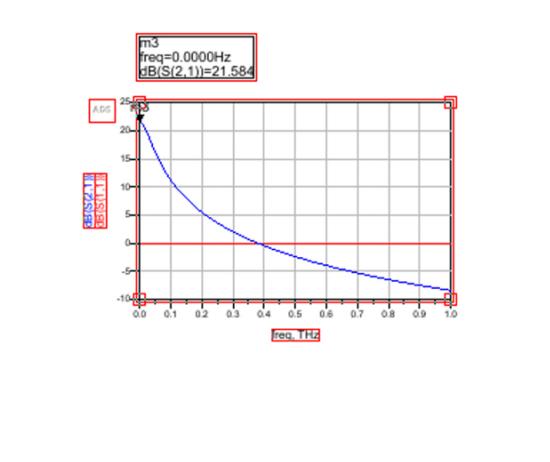
$$Y_{22} = \frac{I_2}{V_2} \Big|_{V_1=0} = G \text{ only current through } S \text{ port.}$$

$$\begin{bmatrix} I_1 \\ I_2 \end{bmatrix} = \begin{bmatrix} j\omega C_{gs} & 0 \\ g_m & G \end{bmatrix} \begin{bmatrix} V_1 \\ V_2 \end{bmatrix}$$



$Y_{11}$ : The value is purely imaginary, which is expected for  $Y_{11} = j\omega C_{gs}$  at  $1T+1j3$ .  $Y_{11} = j(2\pi \cdot 1 \times 10^3 \cdot 100 \times 10^{-15})$   $\approx 0.628$ , which matches the graph.  
 $Y_{12}$ : Both real & imag. Values are 0  
 $Y_{21}$ : only real, since voltage should only be a function of  $g_m$ ,  $g_m = 0.2$   
 $Y_{22}$ : Since  $R_L = 75 \Omega$ , real ( $Y_{22,2}$ ) =  $1/75 = 0.0133$   
NO imag components.

Problem 2 (21A only): Now set  $R_i=0$  Ohms,  $gm=200$  mS,  $R_{ds}=75$  Ohms,  $C_{gs}=100$  fF,  $C_{gd}=5$  fF. The source is grounded.



- a) Again compute the 4 Y-parameters (real and imaginary parts) by hand. Use numerical approximations so that all four of these are written as polynomials in  $(j\omega)$ , and truncate the polynomials to 2nd order in  $(j\omega)$ . Comment about how the effects of the various circuit elements show up in the Y parameters.

b) Again simulate using the provided gain\_testbench, and make plots of the real and imaginary parts of the 4 Y-parameters.

This is an elementary introduction to device model extraction: S-parameters are measured of a transistor, converted to Y-parameters, and compared to that of a model.

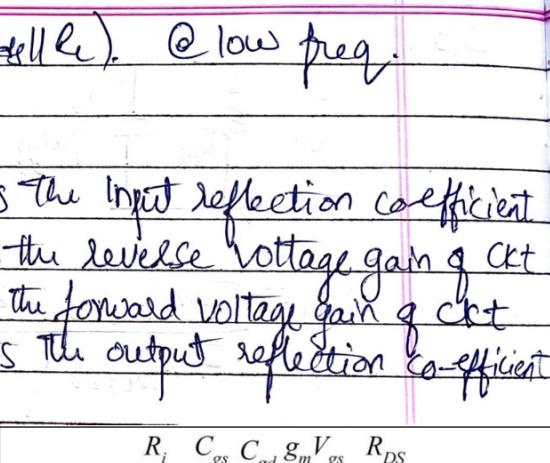
2a)

$$Y_{11} = \frac{I_1}{V_1} \Big|_{V_2=0} = j\omega(C_{gs} + C_{gd})$$

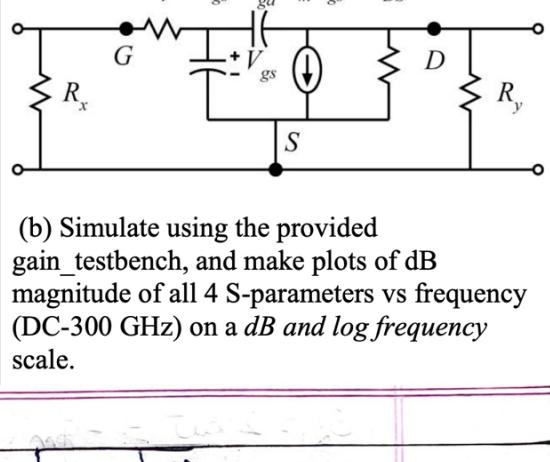
$$Y_{12} = \frac{I_1}{V_2} \Big|_{V_1=0} = -j\omega C_{gd}; \text{ reverse admittance based on the fact that transistor has gate-drain cap.}$$

$$Y_{21} = \frac{I_2}{V_1} \Big|_{V_2=0} = g_m - j\omega C_{gd}$$

$$Y_{22} = \frac{I_2}{V_2} \Big|_{V_1=0} = G_{ds} + j\omega C_{gd}$$



Problem 3:  $R_i=0$  Ohms,  $gm=200$  mS,  $R_{ds}=75$  Ohms,  $C_{gs}=100$  fF. The source is grounded.



Please also make plots on the Smith chart of  $S_{11}$  and  $S_{22}$ , and polar plots of  $S_{21}$  and  $S_{12}$ .

- a) Compute by hand the four S-parameters. Make a hand sketch of dB magnitude of  $S_{11}$  and  $S_{21}$  vs frequency on a log scale (DC-300 GHz).  
b) Simulate using the provided gain\_testbench, and make plots of dB magnitude of  $S_{11}$  and  $S_{21}$  vs frequency (DC-300 GHz) on a log scale.

(c) Explain precisely how  $S_{11}$ ,  $S_{22}$ ,  $S_{21}$  relate to gain, input and output impedance.

3)

$$Z_{in} = j\omega C_{gs} = 50 \Omega$$

$$S_{11} = \frac{V_1}{V_2} \Big|_{V_2=0} = 0$$

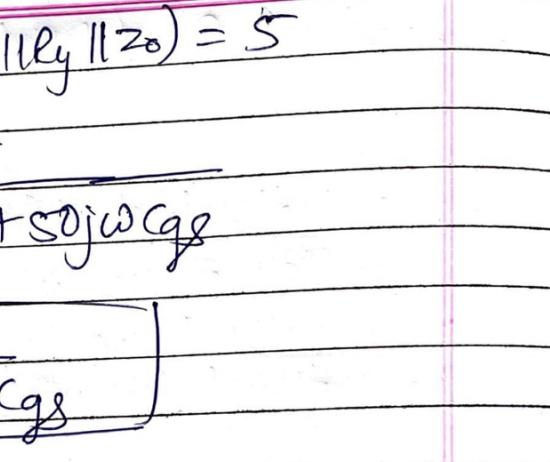
$$S_{21} = \frac{V_2}{V_1} \Big|_{V_1=0} = 2 \frac{V_{out}}{V_{gen}}$$

$$V_{out} = -g_m V_{gen} R_{ds}$$

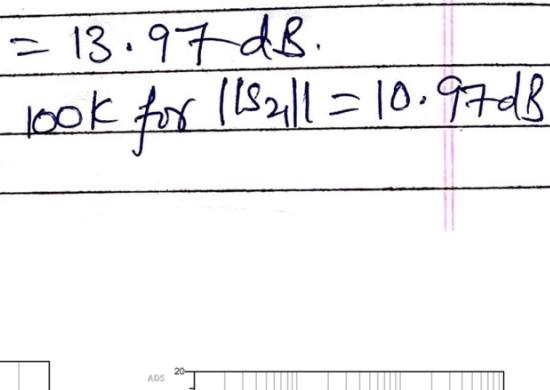
$$V_{out} = -g_m R_{ds} \left( \frac{1}{1+j\omega C_{gs} Z_0} \right) V_1$$

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$$S_{21} = \frac{V_2}{V_1} \Big|_{V_1=0} = Z_{out} - Z_0 = 75 - 50 = 25 \Omega$$

$$S_{22} = \frac{V_2}{V_2} \Big|_{V_1=0} = Z_{out} + Z_0 = 75 + 50 = 125 \Omega$$


Problem 4: (please DO work this problem) Simple common-source amplifier (bias networks are not shown)  $R_i=0$  Ohms,  $gm=200$  mS,  $R_{ds}=75$  Ohms,  $C_{gs}=100$  fF.



- a) Compute the values of  $R_x$  and  $R_y$  necessary to give 50 Ohm input and output impedance. (b) the circuit is then to be used in a 50 Ohm system, i.e. with 50 Ohm generator and load. Compute by hand all four S-parameters.

(c) Explain precisely how  $S_{11}$ ,  $S_{22}$ ,  $S_{21}$  relate to gain, input and output impedance.

4a)

$$Z_{in} = R_x || j\omega C_{gs} = 50 \Omega$$

$$S_{11} = \frac{V_1}{V_2} \Big|_{V_2=0} = 0$$

$$S_{21} = \frac{V_2}{V_1} \Big|_{V_1=0} = 2 \frac{V_{out}}{V_{gen}}$$

$$V_{out} = -g_m V_{gen} R_{ds}$$

$$V_{out} = -g_m R_{ds} \left( \frac{1}{1+j\omega C_{gs} Z_0} \right) V_1$$

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5a)

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(b) Simulate using the provided gain\_testbench, and make plots of the magnitude of all 4 S-parameters vs frequency (DC-300 GHz) on a dB and log frequency scale.

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$$S$$