

Department of Electrical & Computer Engineering
University of California, Santa Barbara

ECE 152A
Summer 2010
Shynk
H.O. #1

ECE 152A COURSE INFORMATION

Title: Digital Design Principles

Lectures: Monday and Wednesday, 12:30–1:50 p.m., Room 387 104

Instructor: Professor John J. Shynk (shynk@ece.ucsb.edu)

Office: HFH 3123, 893-3977

Office Hours: Monday and Wednesday, 2:00–3:00 p.m.

Teaching Assistants: Nilesch Modi (nilesch@umail.ucsb.edu)
Seid Hadi Rasouli (hadi@ece.ucsb.edu)
Wei Tang (w_tang@umail.ucsb.edu)

Labs: Monday and Wednesday, 6:00–8:50 p.m., HFH 1124
Tuesday and Thursday, 2:00–4:50 p.m., HFH 1124
Tuesday and Thursday, 5:00–7:50 p.m., HFH 1124

Grading: Homework (weekly)
Lab (weekly)
Midterm Exam (Wednesday, July 28, 12:30–1:30 p.m.)
Final Exam (Wednesday, August 25, 12:30–1:50 p.m.)

Required Book: Brown and Vranesic, *Fundamental Digital Logic with Verilog Design*, McGraw-Hill, 2nd Edition, 2008

Course Web Site: http://www.ece.ucsb.edu/courses/ECE152/152A_Su10Shynk/